

DIGITAL LOGIC DESIGN PRINCIPLES
Balabanian & Carlson
Errata

Page No.	Correction
30	Problem 19a. ascending should be descending
73	Third paragraph under Design Economy, penultimate line: “and of the” should be “and on the”.
78	Problem 13 c. Insert a prime following the parens.
85	Fig 3c. Reverse the last 2 row headings: 11 to 10 and 10 to 11.
109	Continuation of footnote 22b, the ending $f =$ some expression, all the numerals in the expression should be subscripts.
130	Delete problem 55 and insert it as Problem 35 in Chapter 4.
134	Figure 2. Caps X and Y should be lower case.
139	Figure 8. Output of first square on the left should be G_1 , not C_1 . Blank input of second square should be labeled B_2 .
160	Problem 1(b). Delete “circuit”.
167	Add Problem 35, from Chapter 3, Problem 55.
183	In Figure 14 replace output y with Q and y' with Q' In last line of first paragraph, replace y by Q in three places.
218	Example 8. Last line of first paragraph under “Possible sequences . . .” should be: z: - - - - - 1 1 1 1 0 0 1 1 1 - -
219	Figure 21. The output labeled “x” should be l.c. “z”
256	In the top box, the output labeled “ y_i ” should be cap: “ Y_i ”
257	In Figure 3, spacings of t ’s along axis should be fixed. t_4 should be moved right to the position just under back end of wide pulse in x_2 .. t_7 should be moved left to the position just under back end of wide pulse in x_1 . t_{11} should be moved left to just under the last rise of pulse in x_1 .
273	In last line in next to last paragraph: Insert “2-input” before “AND gate”. Add at end of footnote 8: “Same for Figure 10b.”
317	First line of second paragraph starting on that page: Chapter “3” should be “2”.