As Internet traffic grows and demands for quality of service become stringent, researchers and engineers can turn to this go-to guide for tested and proven solutions. This text presents the latest developments in high performance switches and routers, coupled with step-by-step design guidance and more than 550 figures and examples to enable readers to grasp all the theories and algorithms used for design and implementation.

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FEATURES

Over 550 figures and examples provide clarity to the theories and algorithms used to implement high performance switches and routers.

IPv6 coverage (three chapter sections) enhances your understanding of the necessary transition between IPv4 to IPv6 and the enhancements IPv6 provides.

• Network equipment vendors can use the technologies described in this book to build next generation switches and routers.

• Commercial examples (Vitesse, AMCC, IBM, and Agere) facilitate the hands on presentation of High-Speed Router Chip Sets.

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