DESCRIPTION

As Internet traffic grows and demands for quality of service become stringent, researchers and engineers can turn to this go-to guide for tested and proven solutions. This text presents the latest developments in high performance switches and routers, coupled with step-by-step design guidance and more than 550 figures and examples to enable readers to grasp all the theories and algorithms used for design and implementation.

ABOUT THE AUTHOR

H. Jonathan Chao, PhD, is Department Head and Professor of Electrical and Computer Engineering at Polytechnic University, Brooklyn, New York. He holds more than twenty-six patents and is an IEEE Fellow. His research focuses on terabit switches and routers, network security, quality of service control, and optical switching.

Bin Liu, PhD, is Professor in the Department of Computer Science at Tsinghua University, Beijing, China. His research interests include high performance switches and routers, network security, network processors, and traffic engineering. Dr. Liu holds more than ten patents in China.
FEATURES

Over 550 figures and examples provide clarity to the theories and algorithms used to implement high performance switches and routers.

IPv6 coverage (three chapter sections) enhances your understanding of the necessary transition between IPv4 to IPv6 and the enhancements IPv6 provides.

- Network equipment vendors can use the technologies described in this book to build next generation switches and routers.
- Commercial examples (Vitesse, AMCC, IBM, and Agere) facilitate the hands on presentation of High-Speed Router Chip Sets.

SERIES

Wiley - IEEE

To purchase this product, please visit https://www.wiley.com/en-us/9780470053676