



# Digital Systems: Hardware Organization and Design, 3rd Edition

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## DESCRIPTION

This heavily revised edition of the well-received text teaches the organization and design of complex digital systems. Unique in its effective exploitation of a hardware description language, this book's systematic approach involves the reader in the design process. Rather than just describe an end product, the authors employ a register-transfer and control-sequence design language--A Hardware Program Language (AHPL), through which the design of a wide variety of digital hardware systems is explored. Text contains much new material, as well as sections on peripherals, interfacing, microprocessors, hardware compiling processes, and virtual memory hardware. Includes extensive examples and problems sets.

## ABOUT THE AUTHOR

Frederick J. Hill is the author of Digital Systems: Hardware Organization and Design, 3rd Edition, published by Wiley. Gerald R. Peterson is the author of Digital Systems: Hardware Organization and Design, 3rd Edition, published by Wiley.