This book begins with the premise that energy demands are directing scientists towards ever-greener methods of power management, so highly integrated power control ICs (integrated chip/circuit) are increasingly in demand for further reducing power consumption.

- A timely and comprehensive reference guide for IC designers dealing with the increasingly widespread demand for integrated low power management

- Includes new topics such as LED lighting, fast transient response, DVS-tracking and design with advanced technology nodes

- Leading author (Chen) is an active and renowned contributor to the power management IC design field, and has extensive industry experience

- Accompanying website includes presentation files with book illustrations, lecture notes, simulation circuits, solution manuals, instructors’ manuals, and program downloads

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Ke-Horng Chen received his Ph.D. in electrical engineering from National Taiwan University, Taipei, Taiwan, in 2003. From 1996 to 1998, he was a part-time IC Designer at Philips, Taipei, Taiwan. From 1998 to 2000, he was an Application Engineer at Avanti, Ltd., Taiwan. From 2000 to 2003, he was a Project Manager at ACARD, Ltd., where he was engaged in designing power management ICs. He is the author or coauthor of more than 100 papers published in journals and conferences, and also holds several patents. His current research interests include power management ICs, mixed-signal circuit designs, display algorithm and driver designs of liquid crystal display (LCD) TV, red, green, and blue (RGB) color sequential backlight designs.