Understanding Delta-Sigma Data Converters, 2nd Edition
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DESCRIPTION

This new edition introduces operation and design techniques for Sigma-Delta converters in physical and conceptual terms, and includes chapters which explore developments in the field over the last decade

• Includes information on MASH architectures, digital-to-analog converter (DAC) mismatch and mismatch shaping

• Investigates new topics including continuous-time analog-to-digital converters (ADCs) principles and designs, circuit design for both continuous-time and discrete-time ADCs, decimation and interpolation filters, and incremental ADCs

• Provides emphasis on practical design issues for industry professionals

ABOUT THE AUTHOR

Shanthi Pavan is a Professor of electrical engineering at the Indian Institute of Technology, India, and has been the Editor-In-Chief of the IEEE Transactions on Circuits and Systems, and a Distinguished Lecturer of the IEEE Solid State Circuits Society. He is a Fellow of the Indian National Academy of Engineering.

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