DESCRIPTION

This book offers a comprehensive review of the state-of-the-art in innovative Beyond-CMOS nanodevices for developing novel functionalities, logic and memories dedicated to researchers, engineers and students. It particularly focuses on the interest of nanostructures and nanodevices (nanowires, small slope switches, 2D layers, nanostructured materials, etc.) for advanced More than Moore (RF-nanosensors-energy harvesters, on-chip electronic cooling, etc.) and Beyond-CMOS logic and memories applications.

ABOUT THE AUTHOR

Francis Balestra received the M.S. and Ph.D. degrees in electronics from the Institut Polytechnique, Grenoble, France, in 1982 and 1985, respectively. He is a member of the European Academy of Sciences, of the Advisory Committee of the Chinese Journal of Semiconductors and Chinese Physics B and received the Blondel Medal (French SEE) in 2001. He is also member of the European ENIAC Scientific Community Council and several ENIAC/AENEAS Working Groups. F. Balestra has coauthored over 130 publications in international scientific journals, 240 communications at international conferences (more than 70 invited papers and review articles), and 20 books or chapters.